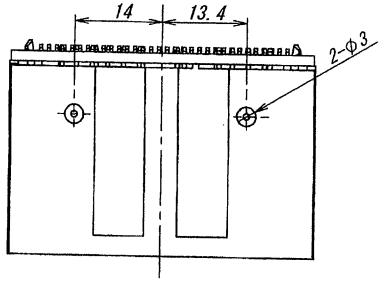
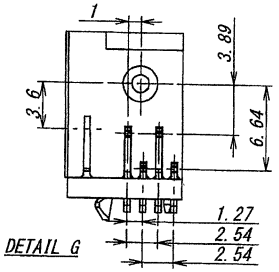
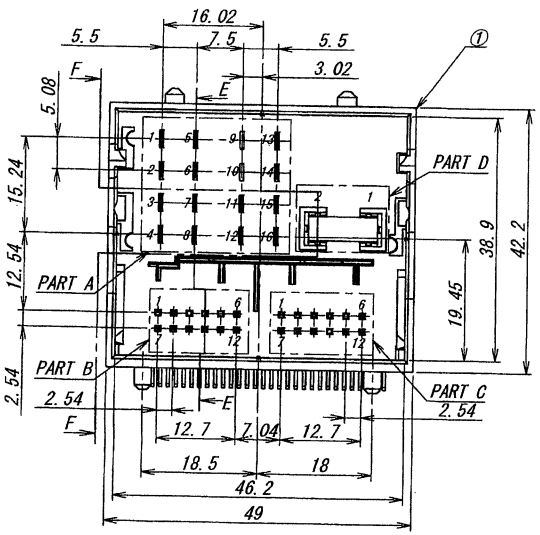
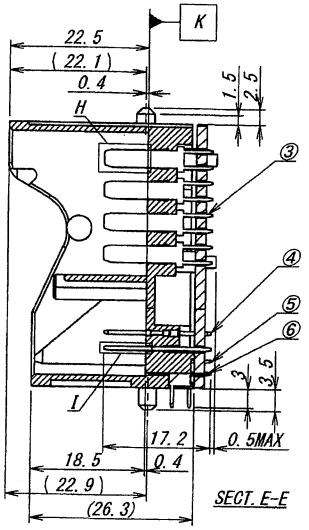




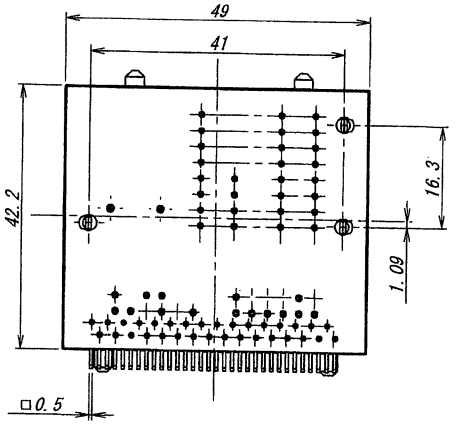
DETAIL G

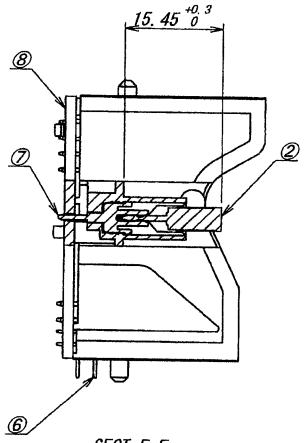
IRS No. 9472B-63Z42-TM	ポスト挿入位置 (POST INSERT POSITION) :ポスト有り(WITH POST)																													
	PART A																													
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16														
	ピン,コンタクト挿入位置 (PIN CONTACT INSERT POSITION) :ピン,コンタクト有り(WITH PIN,WITH CONTACT)																													
	PART B												PART C												PART D					
	1	2	3	4	5	6	7	8	9	10	11	12	1	2	3	4	5	6	7	8	9	10	11	12	1	2				
	PCBポスト挿入位置 (PCB POST INSERT POSITION) :ポスト有り(WITH POST)																													
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32



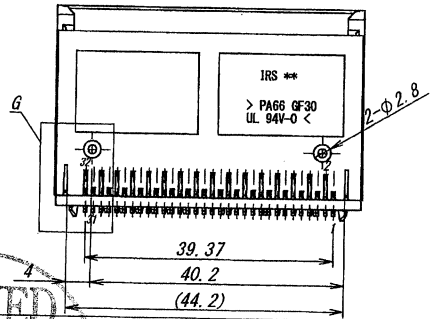


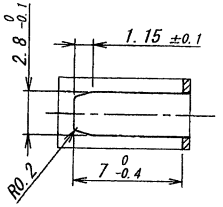
SECT. E-E



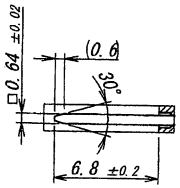


SECT. F-F

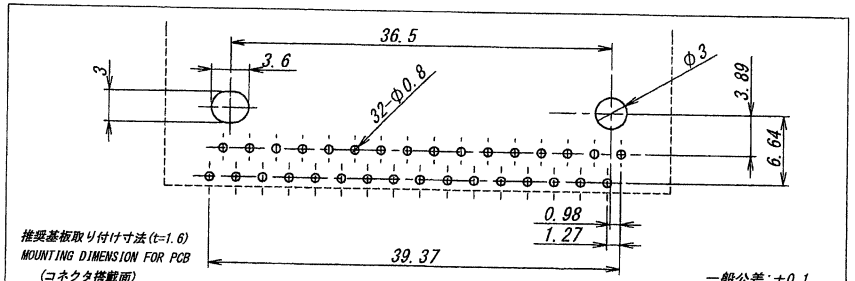




DETAIL H



DETAIL I



ISSUED '12.6.07 IRISO ENG. DEPT.

LTR 記号	ECN No	REVISION RECORD 記事	DR	CHK	DATE 年月日
△1	44153	RELEASED	You	[illegible]	'12-8-8
△2					
△3					
△4					
△5					

MATERIAL 材質
① ベース : 66ナイロン樹脂 GF30% 黒色 UL94V-0
BASE : PA66 GF30% BLACK UL94V-0
② ヒューズ : 15A
③ ポスト/POST : 黄銅/BRASS (t=0.61)
④ ピン/PIN 黄銅/BRASS (□0.64)
⑤ PCBポスト/PCB POST : 黄銅/BRASS (t=0.5)
⑥ PCBポスト/PCB POST : 黄銅/BRASS (t=0.5)
⑦ ヒューズ端子/FUSE CONTACT : 黄銅/BRASS (t=0.7)
⑧ PCB:FR4 UL94V-0 (t=1.6)

FINISH 仕上げ
③錫めっきリフロー処理めっき/TIN PRE-PLATED (REFOLW) :1.0~1.5μm
④ニッケル下地めっき/OVER NICKEL PLATE : 1.0μm MIN.
錫リフローめっき/TIN REFLOW PLATE : 2.0μm MIN.
⑤⑥⑦ニッケル下地/OVER NICKEL PLATE : 2~3μm.
半光沢錫めっき/TIN MATT PLATE : 3~5μm.

GENERAL TOLERANCES 一般公差
DIM 寸法 ± 0.3
ANGLE 角度 ±
3RD. ANG. PROJ. 三角法

SCALE 尺度 /
UNIT 単位 mm
DR '12-8-8 Yougijun
DSN '11-4-26 Shenghui
CHK '12-8-8 [signature]
APP 8/9 J.Kimata

IRISO ELECTRONICS CO.,LTD
イリソ電子工業株式会社

TITLE 名称
9472シリーズ ピンヘッダー
SERIES 9472 PIN HEADER

EDP No

PARTS No 部番
9472B-63Z42-TM

DWG No 図番
IMSA-9472B-63Z42-TM

REV. 0

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