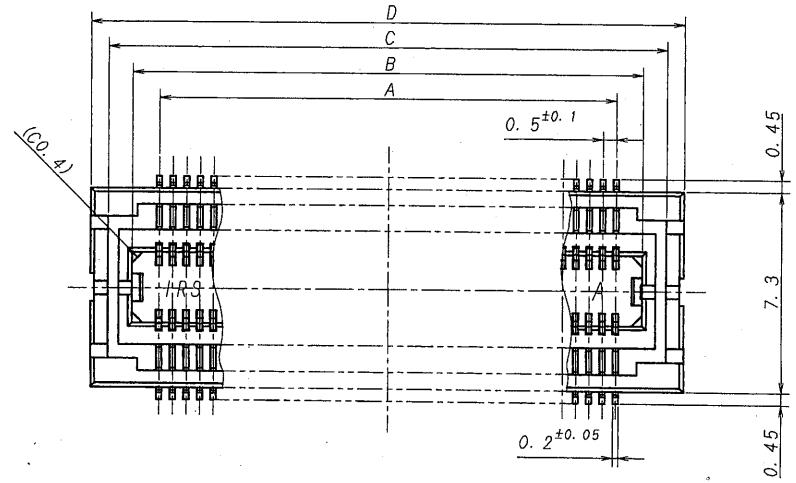
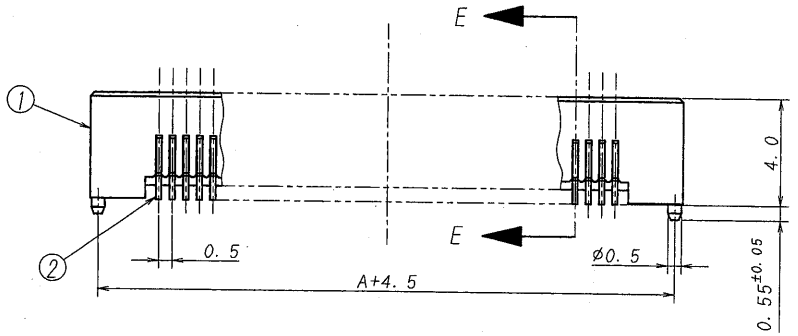
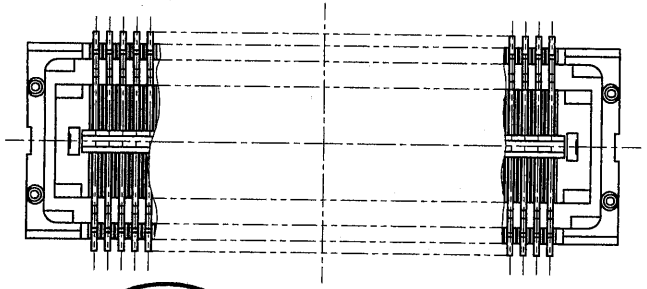
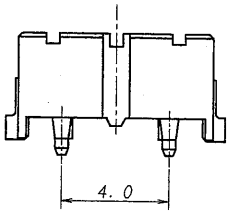
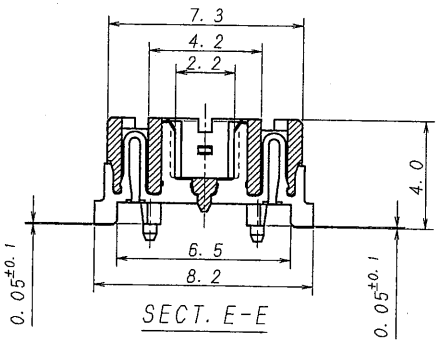










SECT. E-E

CKT	PART No.	A	B	C	D	
30	9842B-30C-GF	7.0	9.0	10.8	12.1	
40	-40C	9.5	11.5	13.3	14.6	
50	-50C	12.0	14.0	15.8	17.1	
60	-60C	14.5	16.5	18.3	19.6	
70	-70C	17.0	19.0	20.8	22.1	
80	-80C	19.5	21.5	22.3	24.6	
90	-90C	22.0	24.0	25.8	27.1	
100	-100C	24.5	26.5	28.3	29.6	
130	9842B-130C-GF	32.0	34.0	35.8	37.1	

△6

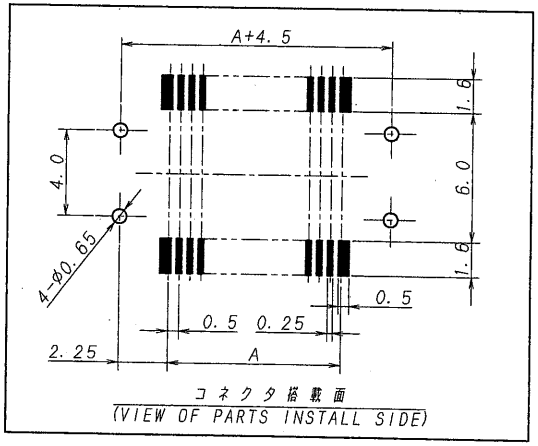
仕 様

△6 1. コプラナリティー : 0.1MAX.
2. 推奨基板寸法 (公差±0.05) 下図参照

NOTE

1. COPLANARITY : 0.1MAX.
2. PCB SIZE (DIMENSION:±0.05) :SEE BELOW FIG.

公差指示なき部分の寸法差	
DIMENTION GAP	
寸法区分	公差
SECTION	DIM.
X< 1mm	±0.15
1mm<X≤ 6mm	±0.2
6mm<X≤ 18mm	±0.3
18mm<X≤ 50mm	±0.5
50mm<X≤120mm	±0.7



コネクタ搭載面
(VIEW OF PARTS INSTALL SIDE)

ISSUED
'10.3.-5
IRISO
ENG. DEPT.

LTR 記号	ECN NO	REVISION RECORD 記事	DR	CHK	DATE 年月日
△6	36602	REVISED	S.N	M.S	10-3-2

MATERIAL 材質
① ベース:ガラス入り耐熱性樹脂, UL94V-0, 黒色
② ポスト:リン青銅 (t=0.2)
① BASE :GLASS FILLED HEAT RESISTING PLASTICS UL94V-0, BLACK
② POST:PHOSPHOR BRONZE (t=0.2)

FINISH 仕上げ
ニッケル下地めっき/OVER NICKEL PLATE 2μmMIN.
金めっき/GOLD PLATE 0.1μmMIN.

GENERAL TOLERANCES 一般公差
DIM 寸法 ± 上表参照 SEE CHART
ANGLE 角度 ± SEE CHART

3RD. ANG. PROJ. 三角法

SCALE 尺度 /
UNIT 単位 mm
DR 10-3-2 Y.Nakagawa
DSN 96-5-17 Takeda
CHK M.Yoshikawa
APP 10-3-2

IRISO ELECTRONICS CO., LTD.
イリソ電子工業株式会社

TITLE 名称
9842シリーズ プラグ (可動タイプ・ボス有)
SERIES 9842 PLUG (FLOATING TYPE/WITH CAP)

EDP No

PARTS No 部番
9842B-**C-GF

DWG No 図番
IMSA-9842B-**C-GF

REV. 6

DO NOT SCALE DRAWING.
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO IRISO AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION