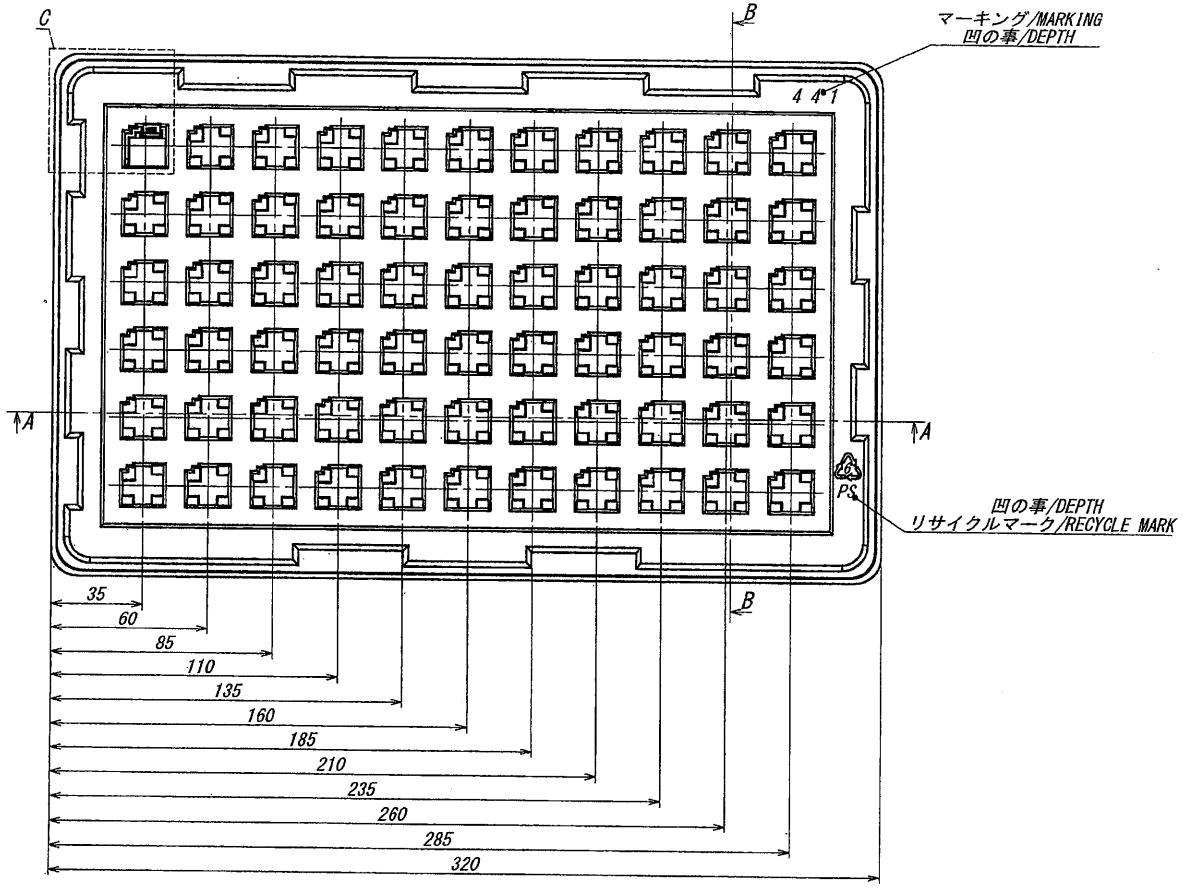
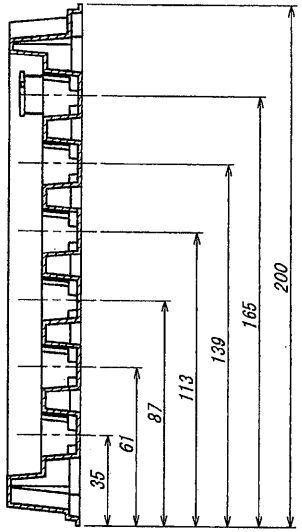
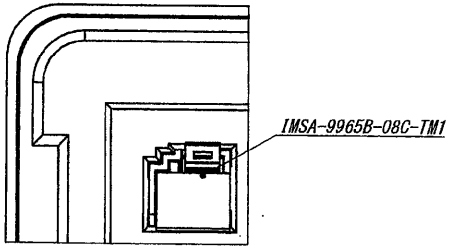
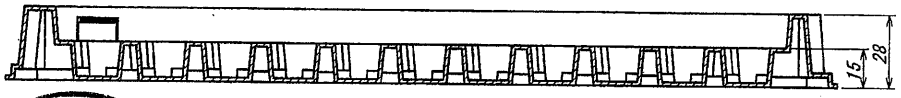




SECT B-B



△ DETAIL C



SECT A-A

NOTE
1. 梱包数量/UNIT QUANTITY : 66ヶ/面.
△6 2. 梱包方法はKS-9965E参照の事
REFER TO KS-9965E FOR THE PACKING METHOD.

ISSUED
'13.11.-1
IRISO
ENG. DEPT.

LTR 記号	ECN No	REVISION RECORD 記事	DR	CHK	DATE 年月日
△6	45073	NOTE REV	Gong	J.S	'13-10-30
△5	43974	NOTE REV	Chen	YOU	'13-5-10
△4	–	RELEASED	Chen	YOU	'13-4-30
△3	–	REVISED	Chen	YOU	'12-11-30

MATERIAL 材質
PS t=1.5
黒色/BLACK
帯電防止材入り/ELECTRIC PROTECT MATERIAL
絶縁抵抗/INSULATE RESISTANCE 10^{4}~10^{8}Ω/300mm

FINISH 仕上げ

GENERAL TOLERANCES 一般公差
DIM 寸法 ± 0.5
ANGLE 角度 ±

3RD. ANG. PROJ. 三角法

SCALE 尺度
UNIT 単位 mm
DR '13-10-30 Gong hanjing
DSN '12-01-31 Chen YongFeng
CHK '13-10-30
APP '13/10/30

IRISO ELECTRONICS CO., LTD
イリソ電子工業株式会社

TITLE 名称
9965シリーズ ピンヘッダー ブリスター梱包
SERIES 9965 PIN HEADER BLISTER PACK PRODUCT

EDP No

PARTS No 部番 △5 IMSA-9965B-08Y501

DWG No 図番 △5 120-209965-001

REV. 6

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